

ABSTRACT OF THE DISCLOSURE

A digitizer is provided which includes: N A/D converters for converting an analog signal output from an electronic device to digital signals at different sampling timings by turns (N is an integer equal to or larger than 2); and N digital filters for outputting corrected signals obtained by multiplying the digital signals output from the N A/D converters by correction coefficients, wherein the correction coefficients are based on phase errors between ideal sampling timings at which associated A/D converters are to sample the analog signal and actual sampling timings at which the N A/D converters sampled the analog signal, respectively.